



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Taketo WATANABE et al.**

Group Art Unit: **2814**

Serial No.: **10/633,538**

Examiner: **Hoai V. Pham**

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Confirmation No.: **6451**

For: **SEMICONDUCTOR DEVICE, MANUFACTURING METHOD  
THEREOF, AND CMOS INTEGRATED CIRCUIT DEVICE**

Attorney Docket Number: **030927**

Customer Number: **38834**

**AMENDMENT UNDER 37 C.F.R. §1.312**

Mail Stop Issue Fee  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

December 15, 2005

Sir:

Subsequent to issuance of a Notice of Allowance in the above-referenced patent application, the following amendments and remarks are respectfully submitted.

Amendments to the Claims begin on page 2 of this paper.

Remarks/Arguments begin on page 9 of this paper.